

FDP-34	VHDL PROGRAMMING AND IMPLEMENTATION IN FPGA	17.06.2019 to 21.06.2019
<p>OBJECTIVES:</p> <ul style="list-style-type: none"> ➤ Write VHDL programs to develop combinational and sequential digital circuits ➤ Simulate the VHDL program ➤ Synthesis the digital circuits in FPGA ➤ Implement real-time applications in FPGA <p>PARTICIPANTS:</p> <p>Teachers of Electrical, Electronics & Communication, Computer Science and Instrumentation disciplines having digital electronics knowledge.</p> <p>INPUT:</p> <p>Digital design with VHDL language - Basic concepts - Gate Level Modeling - Data Flow Modeling - Behavioural level Modeling - Combinational logic circuit design - Sequential logic circuit design - Finite State Machine - Memory - Applications – Vending machine controller – Digital clock - Simulation –Test Bench Code - Synthesis in FPGA.</p> <p>PROCESS:</p> <ul style="list-style-type: none"> ➤ Lecture ➤ Demonstration ➤ Laboratory sessions <p>OUTPUT:</p> <p>The Participants will be able to write VHDL programme for digital logic circuits and implement using FPGA.</p> <p>RESOURCE PERSONS:</p> <ul style="list-style-type: none"> ➤ Dr. G.A. Rathy ➤ Guest Faculty 		
COORDINATOR	VENUE	LAST DATE FOR RECEIPT OF APPLICATIONS
Dr. P.Sivasankar	NITTTR, Chennai	30 days prior to the start of the programme